

6

5

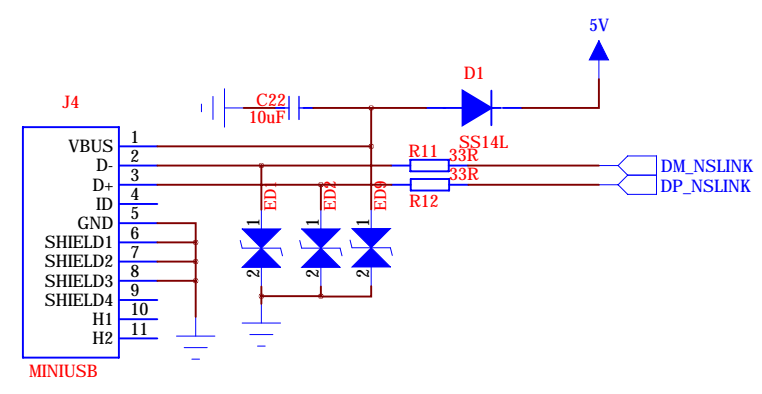
4

3

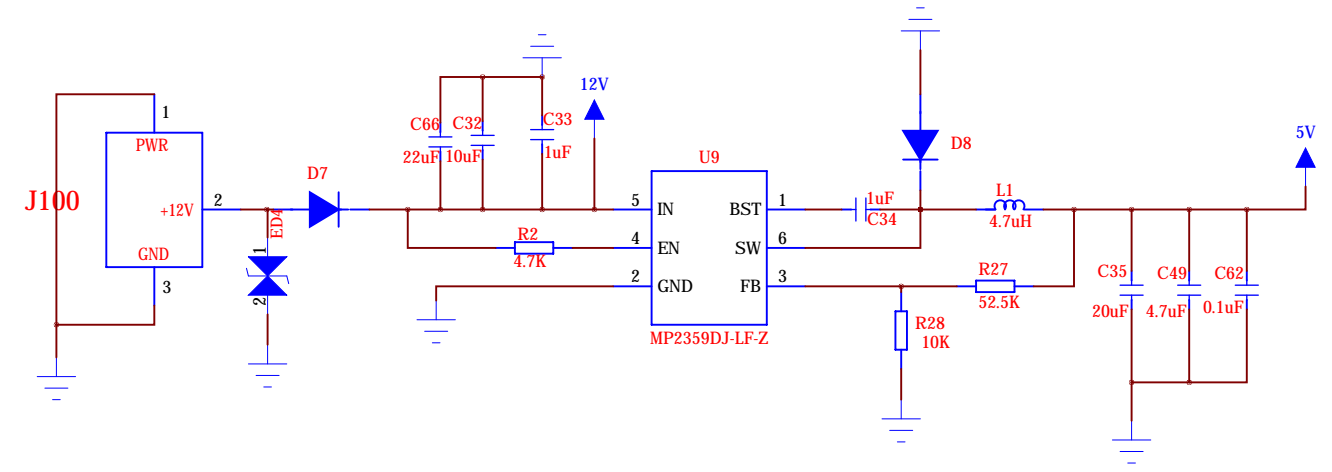
2

1

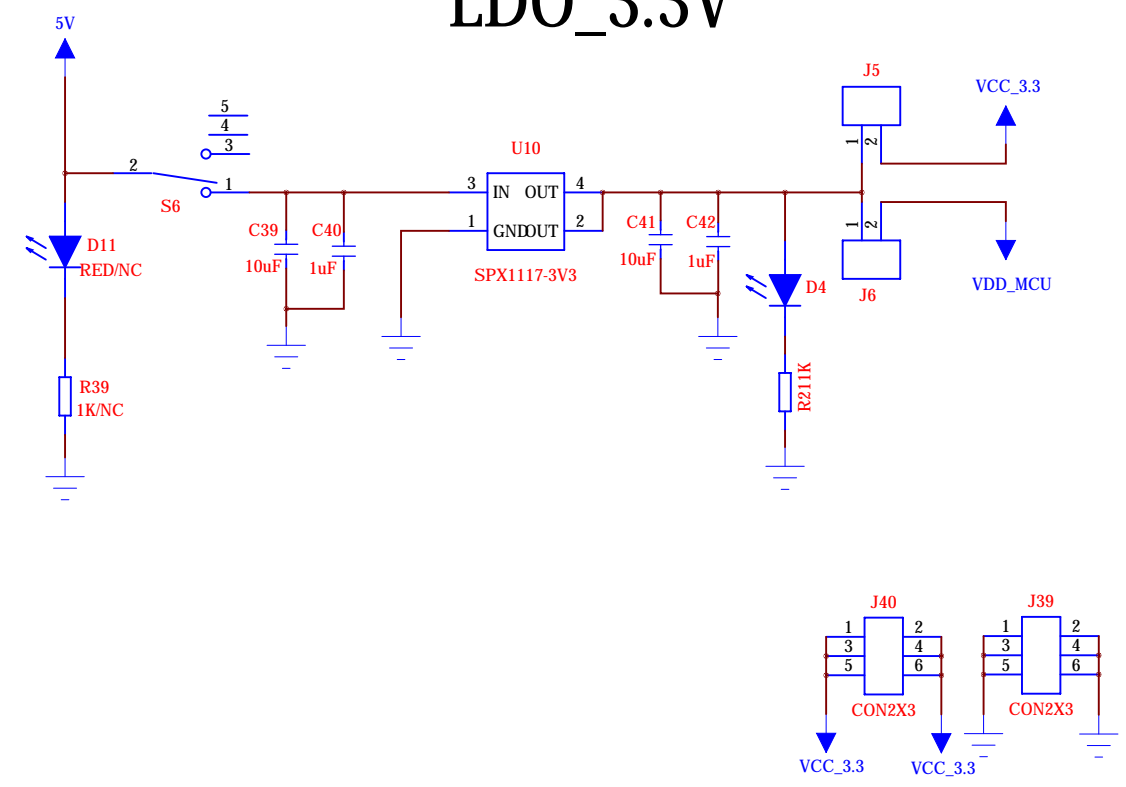
# DEBUG USB



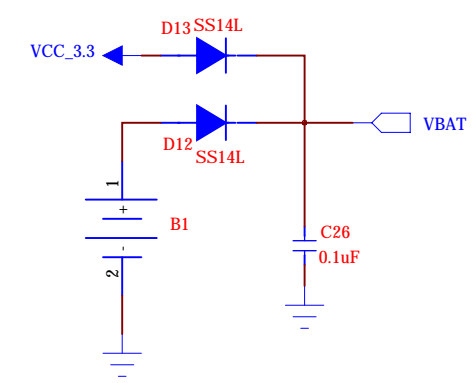
# DC-JCAK




# LDO\_3.3V



# VBAT



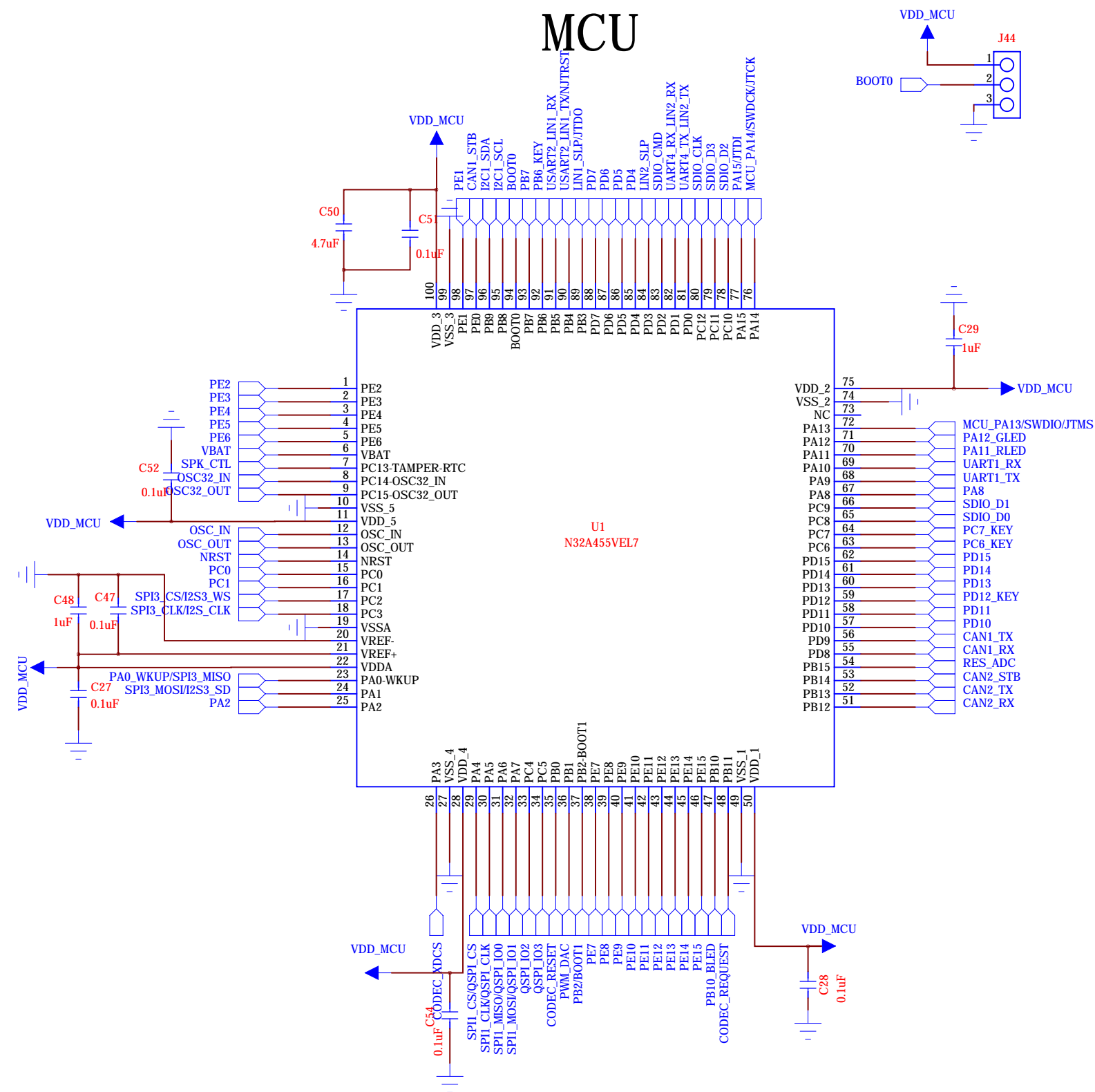


**Nation**

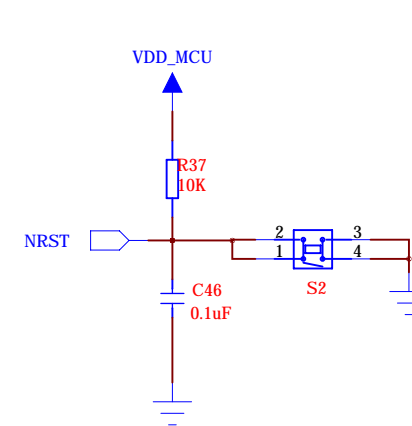
TITLE: **N32A455VEL7-EVB**

SIZE: <b>A3</b>	REV: <b>V1.0</b>
DATED: <b>2020-06-18</b>	SHEET: <b>1 OF 7</b>

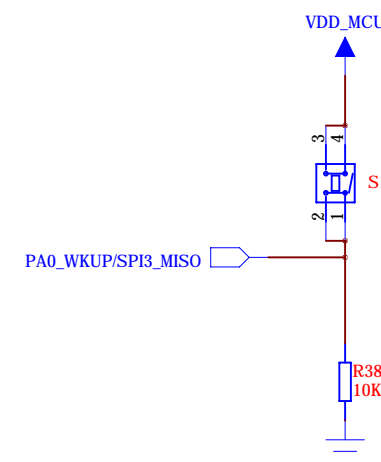
## MCU



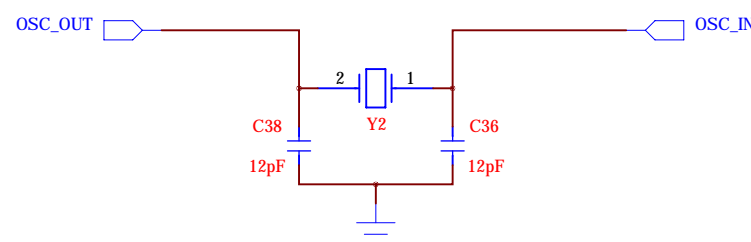
## RESET



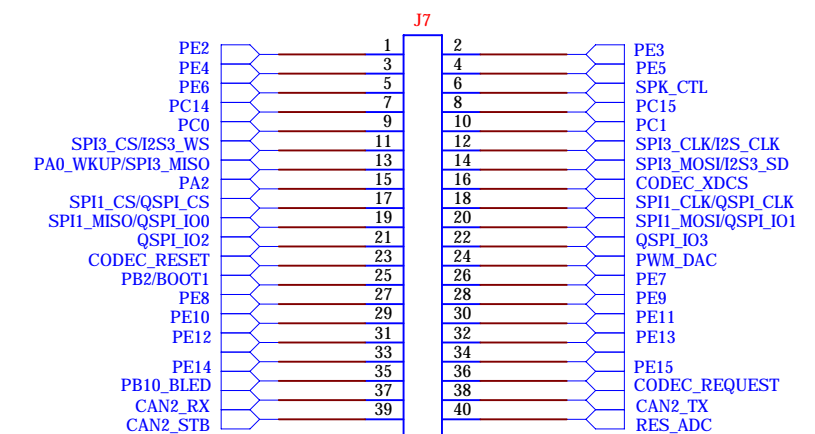
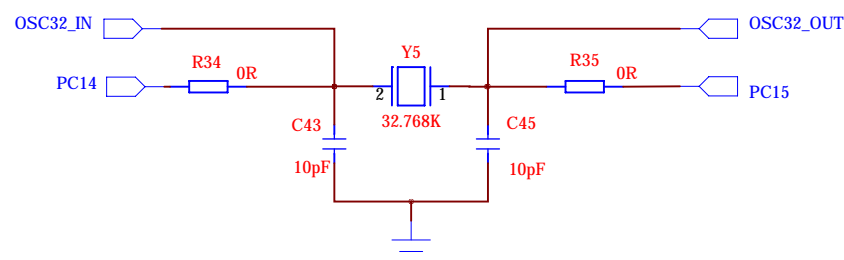
## MCU WAKEUP



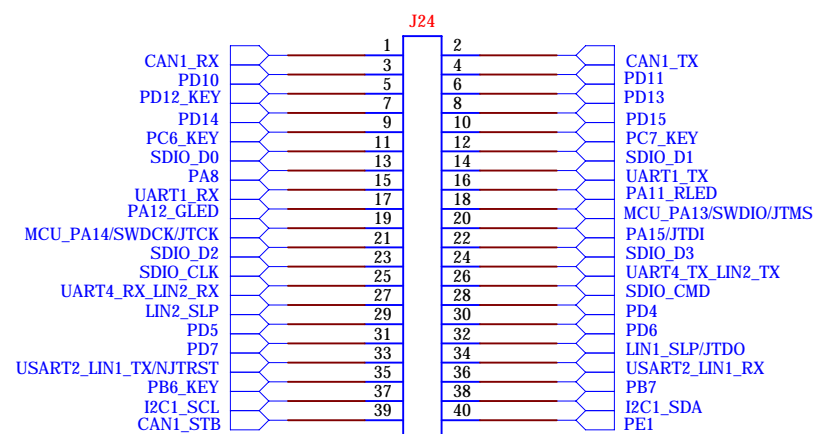
## 8M Crystal



## 32.768K Crystal



HEAD2X20



HEAD2X20



TITLE: N32A455VEL7-EVB

SIZE: A3

REV: V1.0

DATED: 2020-06-18

SHEET: 2 OF 7

6

5

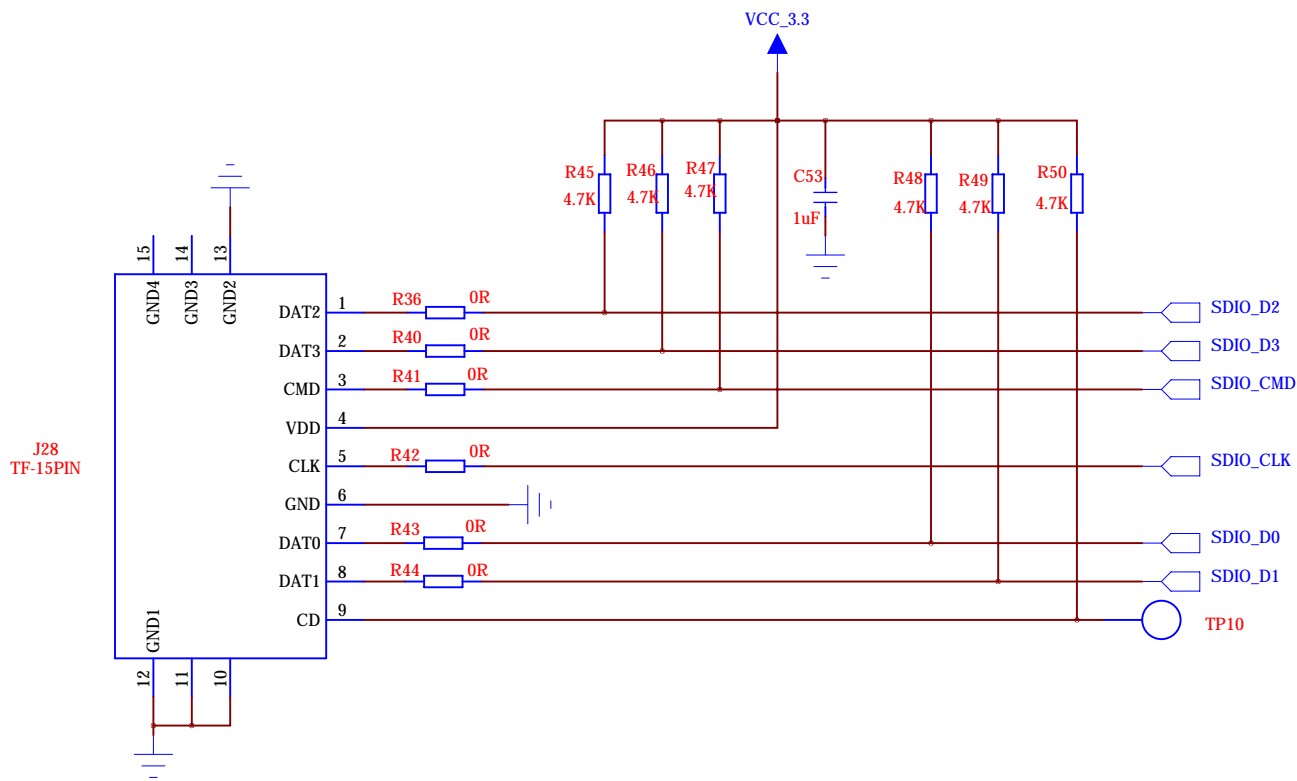
4

3

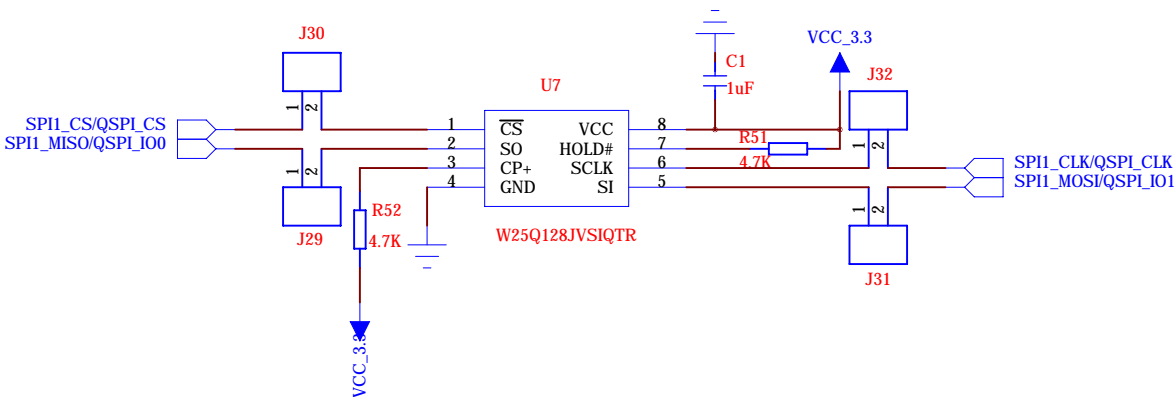
2

1

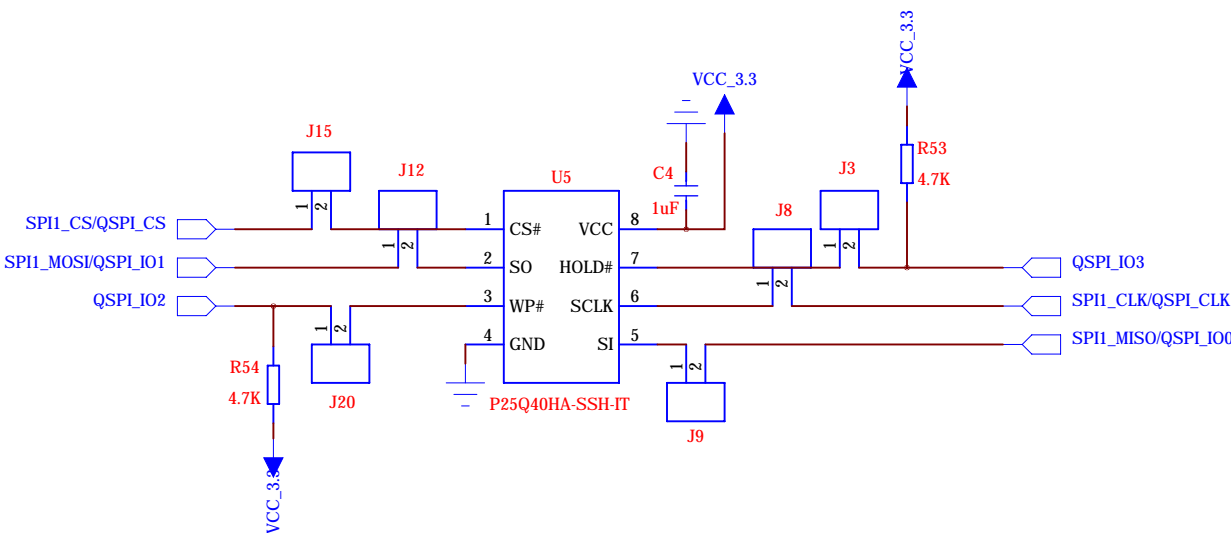
TF1-Card



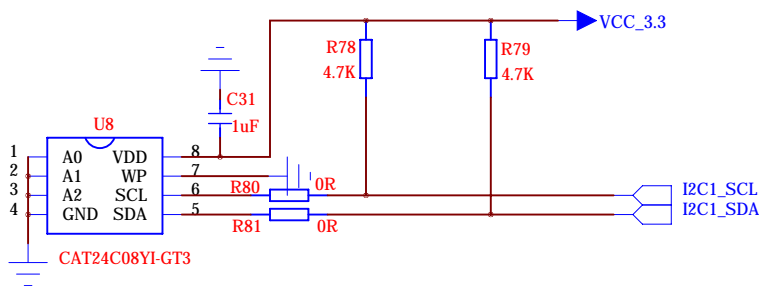
SPI\_FLASH



QSPI\_FLASH



EEROM



TITLE: N32A455VEL7-EVB

SIZE: A3 REV: V1.0

DATED: 2020-06-18 SHEET: 3 OF 7

6

5

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1

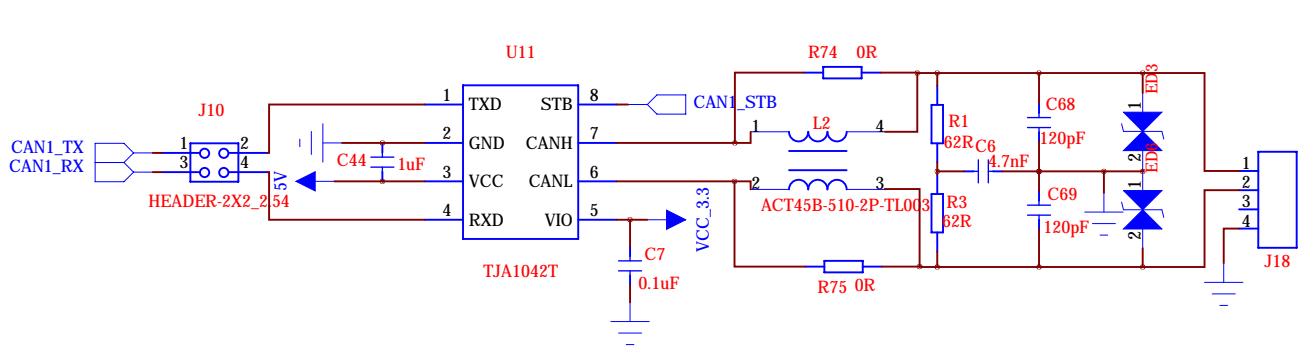
D

C

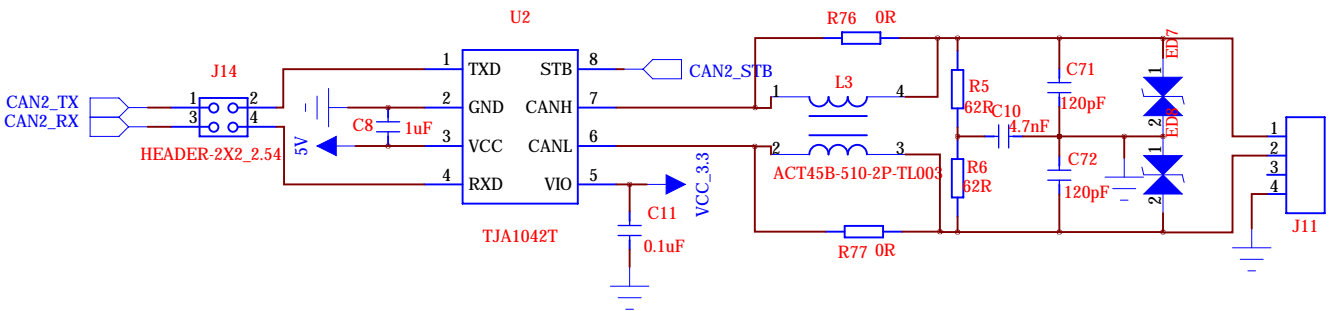
B

A

# CAN1



# CAN2



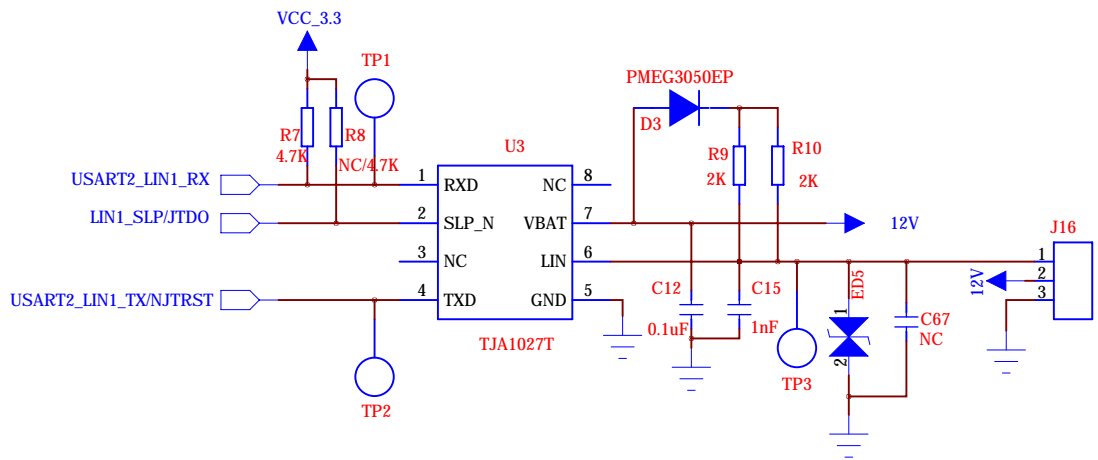
D

C

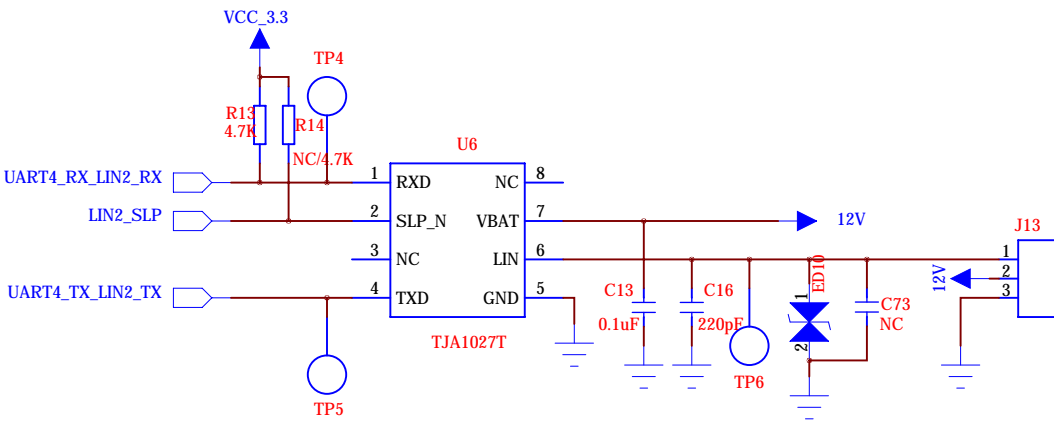
B

A

# MASTER\_LIN

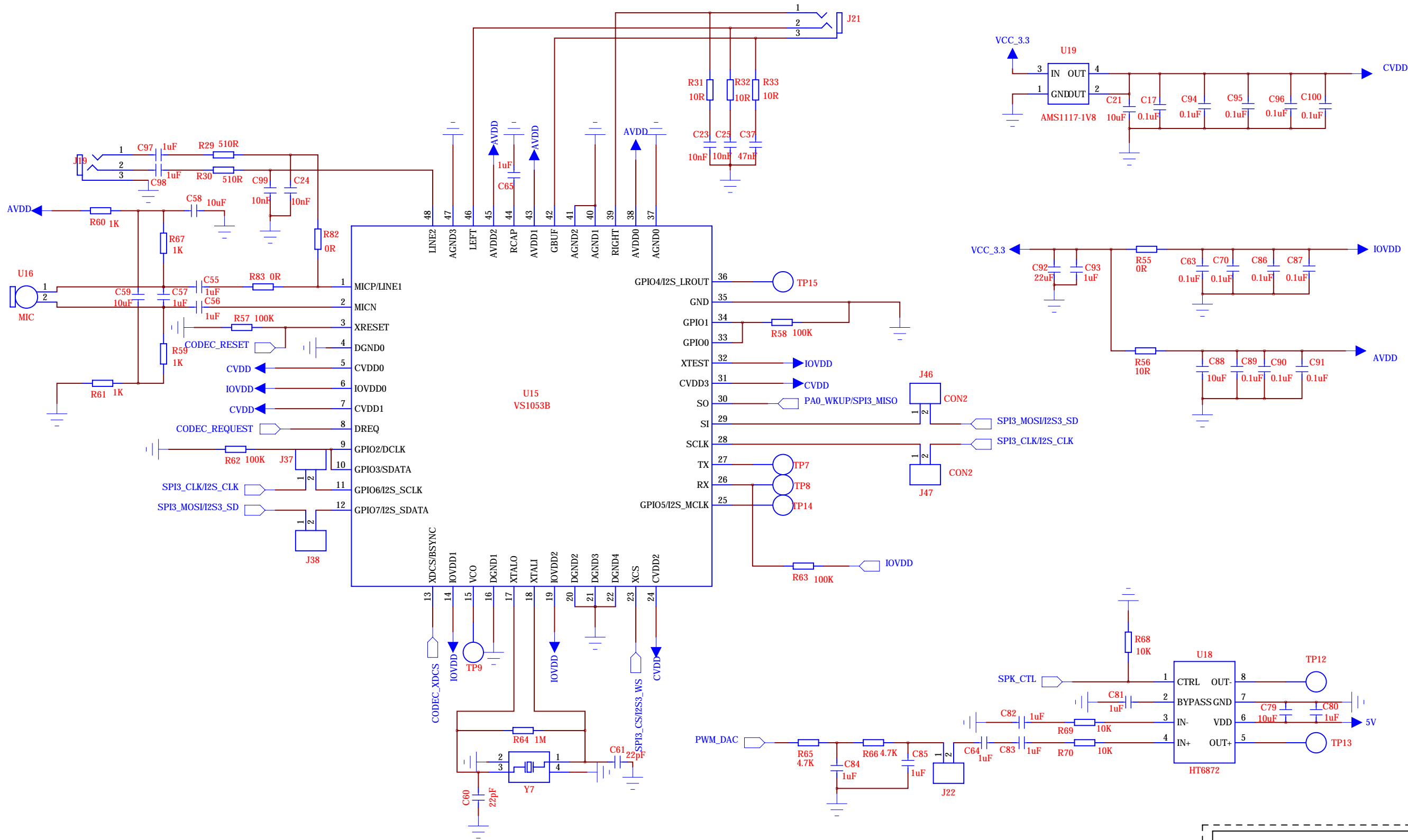


# SLAVE\_LIN



TITLE: N32A455VEL7-EVB			
SIZE:	A3	REV:	V1.0
DATED:	2020-06-18	SHEET:	4 OF 7

CODEC



6

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D

D

C

C

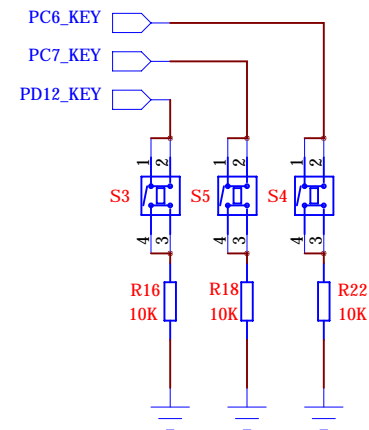
B

B

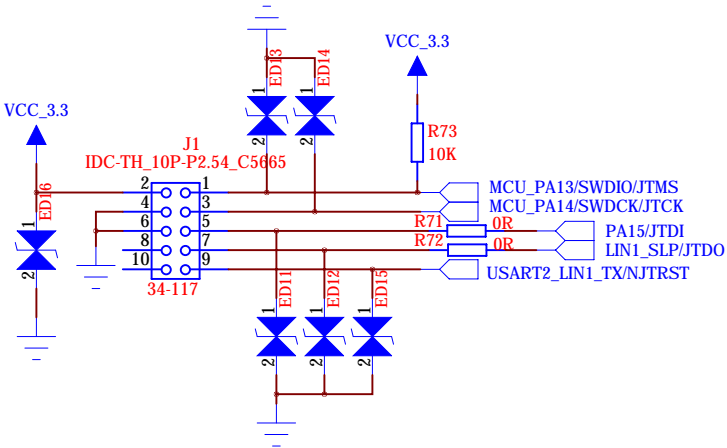
A

A

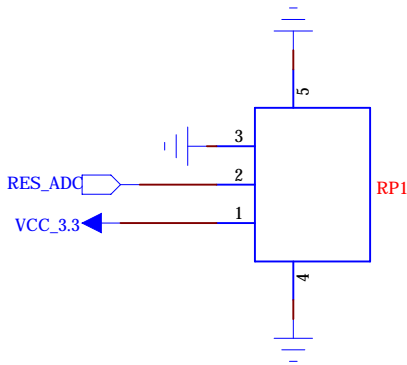
# KEY



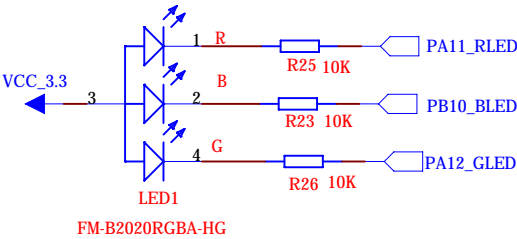
# JTAG



# ADC



# LED



TITLE: N32A455VEL7-EVB	
SIZE: A3	REV: V1.0
DATED: 2020-06-18	SHEET: 6 OF 7

6

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4

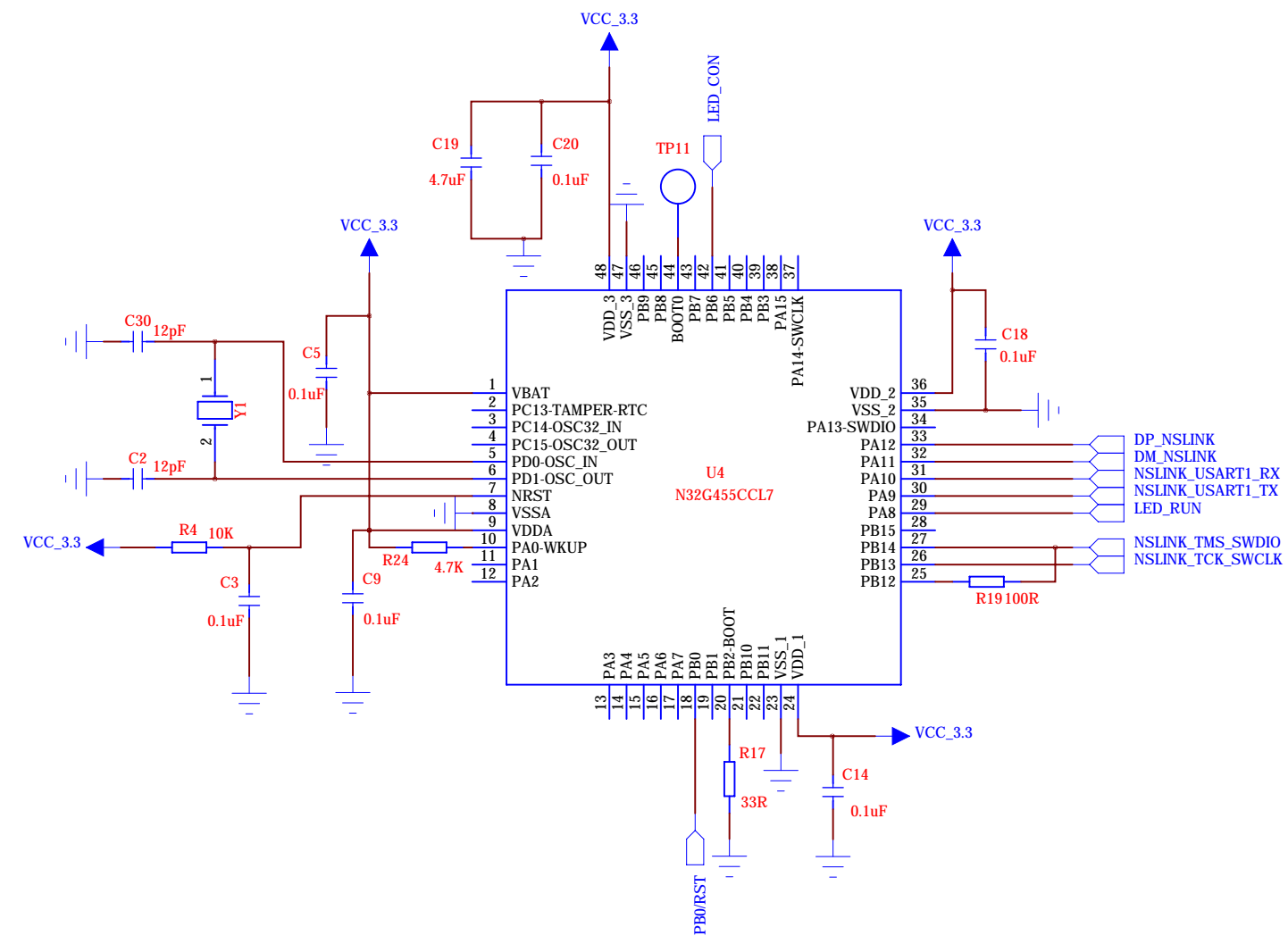
3

2

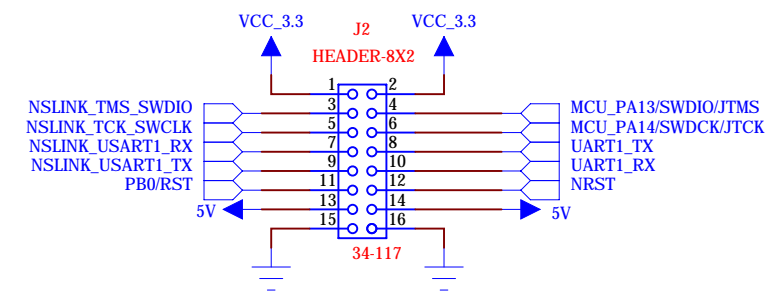
1

# NS-LINK

## MCU



## HEADER



## LED

