

# **N32G003 Series**

## **Errata Sheet**

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# 1 Errata List

## Errata Overview

Errata Link		Chip Version	
		A	B
Ch 2: Power Control (PWR)	2.1: Power Up and Down Failure Problem	●	-
	2.2: Option Byte Configuration LVR Function Cannot Be Used	●	-
Ch 3: GPIO and AFIO	3.1: PA0 Affects Reset When Used as a Normal GPIO	●	-
	Section 3.2: PA9 Cannot Be Used as UART2_RX	●	-
Ch 4: Flash	Section 4.1: Main Flash Area beyond 3kB Can Be Read\Written\Erased by SWD under RDP L1	●	-

Notes:

- 1) ●: *problem present*
- 2) -: *problem absent*

## **2 Power Control (PWR)**

### **2.1 Power Up and Down Failure Problem**

#### **Description**

In a low-temperature environment, the chip may experience occasional power-on and power-off failures. This can result in the chip being unable to start up normally.

#### **Workaround**

When power down, ensure that the chip VDD voltage drops below 100mV, and then power on to the normal working voltage.

### **2.2 Option Byte Configuration LVR Function Cannot Be Used**

#### **Description**

The LVR voltage threshold bit and enable bit are configured via the option byte, but no reset occurs when the actual voltage falls below this threshold.

#### **Workaround**

The LVR reset is enabled by configuring the LVR reset enable bit of the PWR module register.

## **3 GPIO and AFIO**

### **3.1 PA0 Affects Reset When Used as a Normal GPIO**

#### **Description**

When the PA0 pin turns off the Reset function (used as a normal GPIO) through the configuration option byte, the NVIC system soft reset and watchdog reset will not take effect.

#### **Workaround**

Do not configure PA0 for normal GPIOs.

### **3.2 PA9 Cannot Be Used as UART2\_RX**

#### **Description**

Pin PA9 cannot be used as UART2\_RX, and does not support serial port reception.

#### **Workaround**

Do not use PA9 as UART2\_RX.

## **4 Flash**

### **4.1 Main Flash Area beyond 3kB Can Be Read\Written\Erased by SWD under RDP L1**

#### **Description**

Under the L1 read protection level, the area beyond 3kB of the Flash main memory can be read\written\erased by SWD.

#### **Workaround**

No solution is provided for this issue.

## 5 Version History

Version	Date	Changes
V1.0.0	2023.2.27	Initial release
V1.1.0	2023.3.31	Add L1 level SWD access instructions
V1.2.0	2023.7.17	1) Added errata for hardware revision B 2) Added Section 3.2 PA9 usage problem

## 6 Disclaimer

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