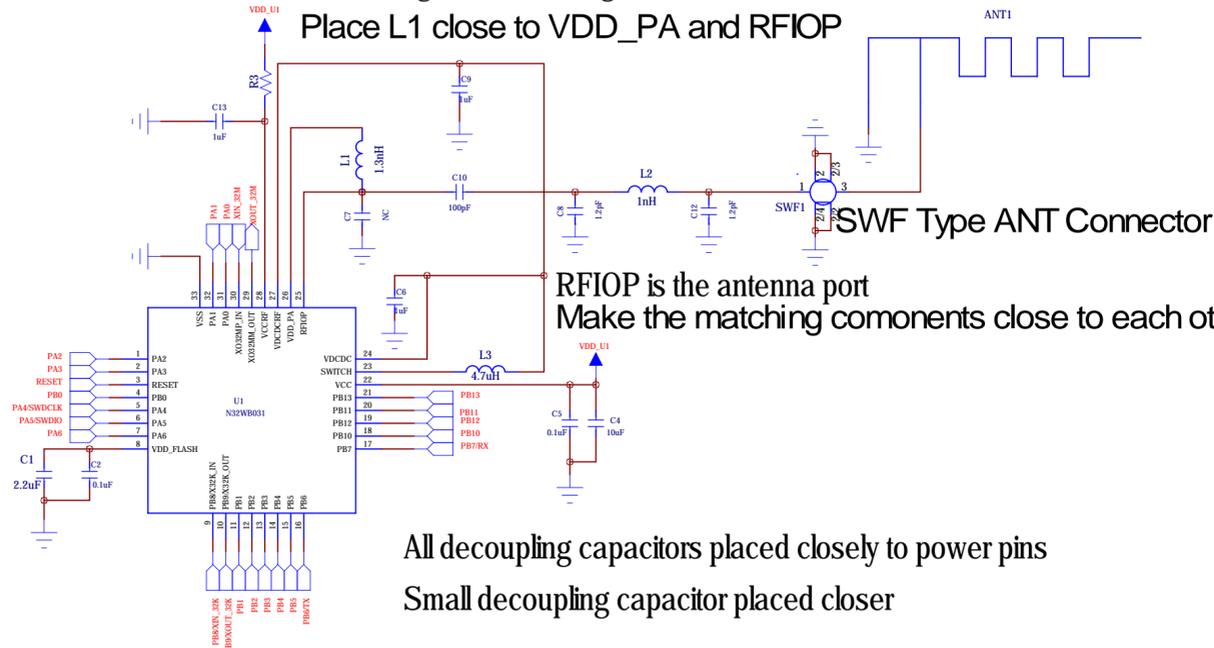


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

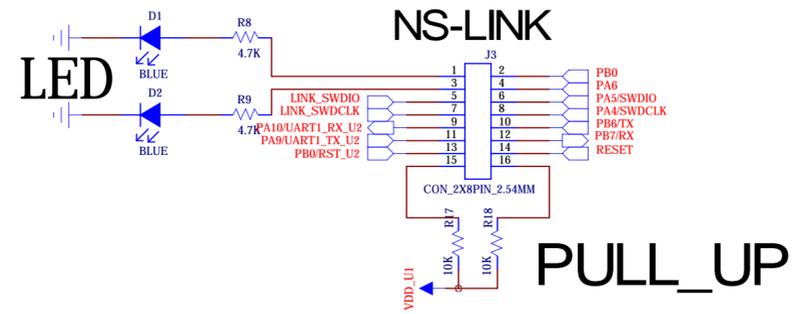
# BlueMCU

VDD\_PA generates voltage to bias internal PA  
Place L1 close to VDD\_PA and RFIOP



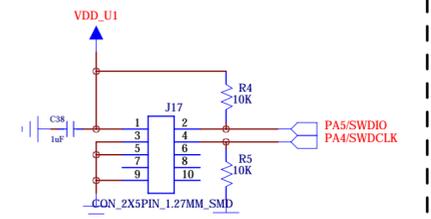
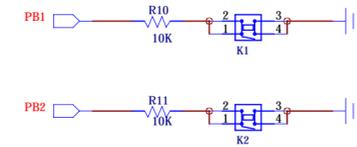
RFIOP is the antenna port  
Make the matching components close to each other

All decoupling capacitors placed closely to power pins  
Small decoupling capacitor placed closer



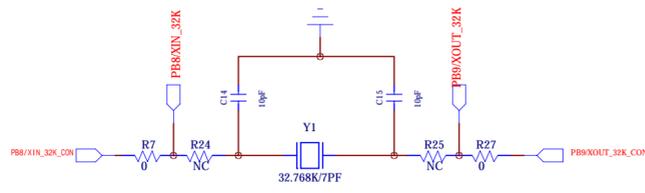
## PULL\_UP

## KEY

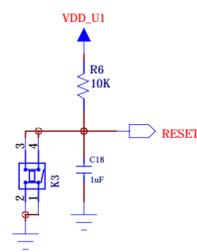


## J-Link Connector

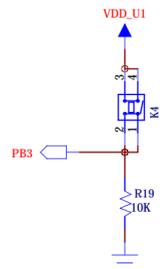
## LSE



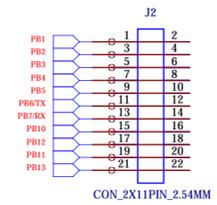
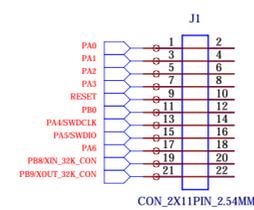
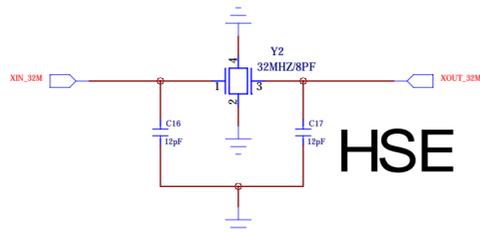
## RESET



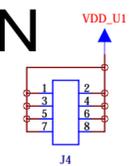
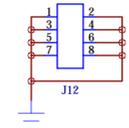
## WAKEUP



## HSE



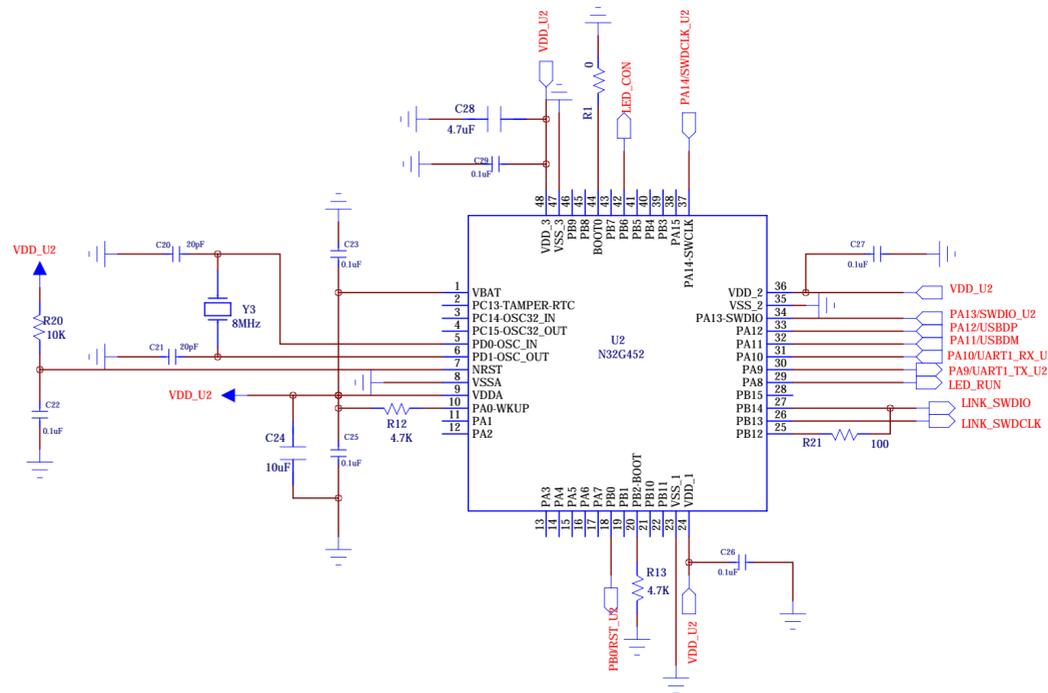
## Connector PIN



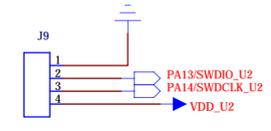
TITLE:	N32WB031_STB		
MODEL NO:		REV:	V1.3
CUSTOMER:		DRAWN:	Nations Technologies Inc.
SHEET: 1 OF 2		CHKD:	
DATED: 2022-09-30		APPD:	
COMPANY:			

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

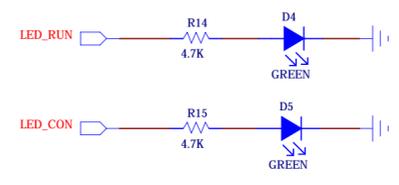
### NS-LINK



### SWD\_U2

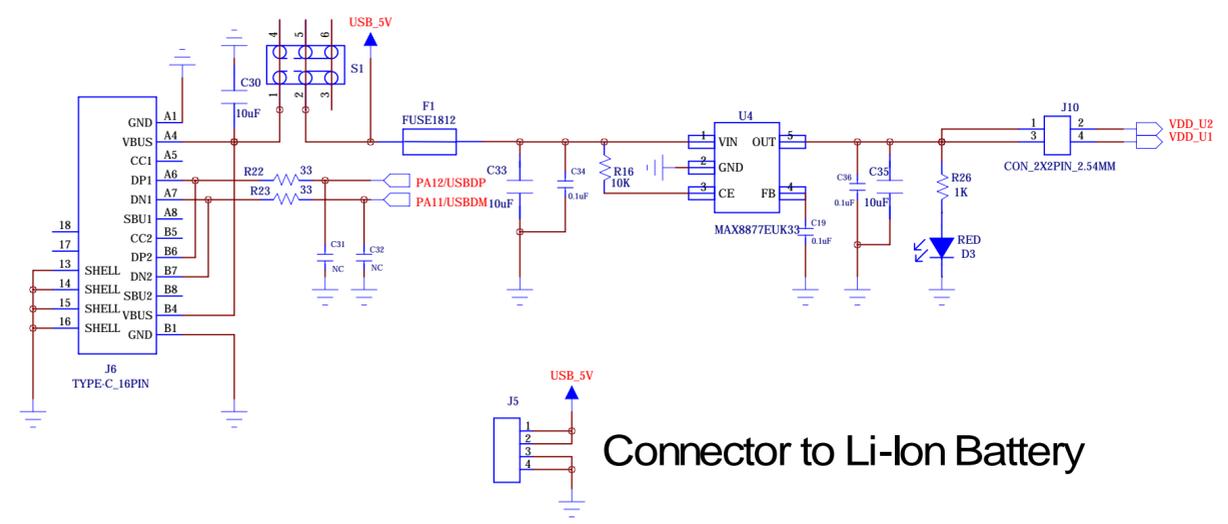


### NS-LINK\_LED

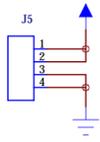


### TYPE-C USB 5V IN

### LDO\_3.3V



### Connector to Li-Ion Battery



TITLE:	N32WB031_STB		
MODEL NO:		REV:	V1.3
CUSTOMER:		DRAWN:	Nations Technologies Inc.
SHEET: 2 OF 2		CHKD:	
DATED: 2022-09-30		APPD:	
COMPANY:			